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APPLICANTS

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** CONTINUING DATA *****

This application is a CON of 10/113,692 03/28/2002 PAT 6,660,083
 which claims benefit of 60/280,604 03/30/2001
 and claims benefit of 60/283,743 04/13/2001
 This application 10/623,375
 claims benefit of 60/398,176 07/24/2002

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY **
 ** 01/05/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY MD	SHEETS DRAWING 3	TOTAL CLAIMS 34	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after				
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>		

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TITLE

Manufacturing methods for semiconductor devices with multiple III-V material layers